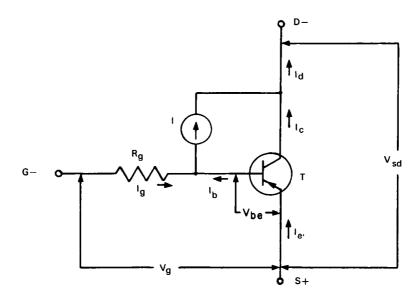
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# NASA TECH BRIEF



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## Equivalent Circuit for a Field Effect Transistor Established for Computer Simulation



## The problem:

To provide a computer simulation for a field effect transistor. Existing computer programs which perform automated electrical circuit analyses and syntheses are only able to simulate resistors, voltage and current sources, diodes, capacitors, inductors, and conventional transistors.

## The solution:

An equivalent circuit for the field effect transistor made up of circuit elements which can be simulated by existing computer programs.

### How it's done:

The field effect transistor equivalent circuit consists of a conventional transistor (T), a resistor (R<sub>g</sub>),

and a constant current source (I). The terminals S, G, and D correspond to the source, gate, and drain terminals of the field effect transistor. The value of  $R_{\rm g}$  is made to be the same as the gate input resistance. The value of I must be calculated from the electrical characteristics of the field effect transistor. Transistor T will be a PNP type for a P-type channel field effect transistor, and an NPN type for an N-type channel. Because computer programs simulate transistors by the  $I_{\rm c}$  vs  $V_{\rm be}$  characteristic curve, the values for  $I_{\rm c}$ ,  $V_{\rm be}$  and  $\beta$  must be calculated for the transistor.

Depending on the accuracy required by the application, improvements to the model may be made by including some or all of the factors which influence

(continued overleaf)

performance characteristics of the field effect transistor. These factors are:

- 1. Adjust the characteristics of the base-collector junction to improve the drain current curve in the pinch-off region.
- 2. Vary the value of  $R_g$  with variation in  $V_g$  to cause the slope of the drain current curve to vary with gate voltage.
- 3. Vary both  $R_g$  and  $\beta$  with temperature.
- 4. Vary  $\beta$  with drain current.

These factors may also be described by curves in the computer program instead of by fixed parameters.

#### Note:

Inquiries concerning this innovation may be directed to:

Technology Utilization Officer Marshall Space Flight Center Huntsville, Alabama 35812 Reference: B66-10690

## Patent status:

No patent action is contemplated by NASA.

Source: L. J. Ming of International Business Machines, Inc. under contract to Marshall Space Flight Center (M-FS-1752)